IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Charle' R. **RUPP**   Examiner: James H. Cho

Serial No.: 09/475,400   Art Unit: 2819

Filed: December 30, 1999

Title: MULTI-SCALE PROGRAMMABLE ARRAY

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May 9, 2001

AMENDMENT

Hon. Commissioner for Patents
Washington, D.C. 20231

Sir:

 In response to the Office Action mailed March 29, 2001, kindly enter the following amendments and remarks.

IN THE CLAIMS

Please delete claims 1-9 without prejudice or disclaimer.

Please enter the following amended claims:

10. (Amended) A programmable logic device, comprising:

 a function cell that provides a result logic value in response to one or more input logic values and a function vector, the function cell being operable to receive the function vector both from a configuration memory and from dynamic configuration signals,

 wherein the function cell includes a function selection block that is operable to selectively receive the function vector in response to a received function overlay enable signal.

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ASI-001; Atty. Dkt. 8894-259572
Rupp; Ser. No. 09/475,400

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